

Fig 3

FIG. 4 is a cross-sectional view of a semiconductor device.

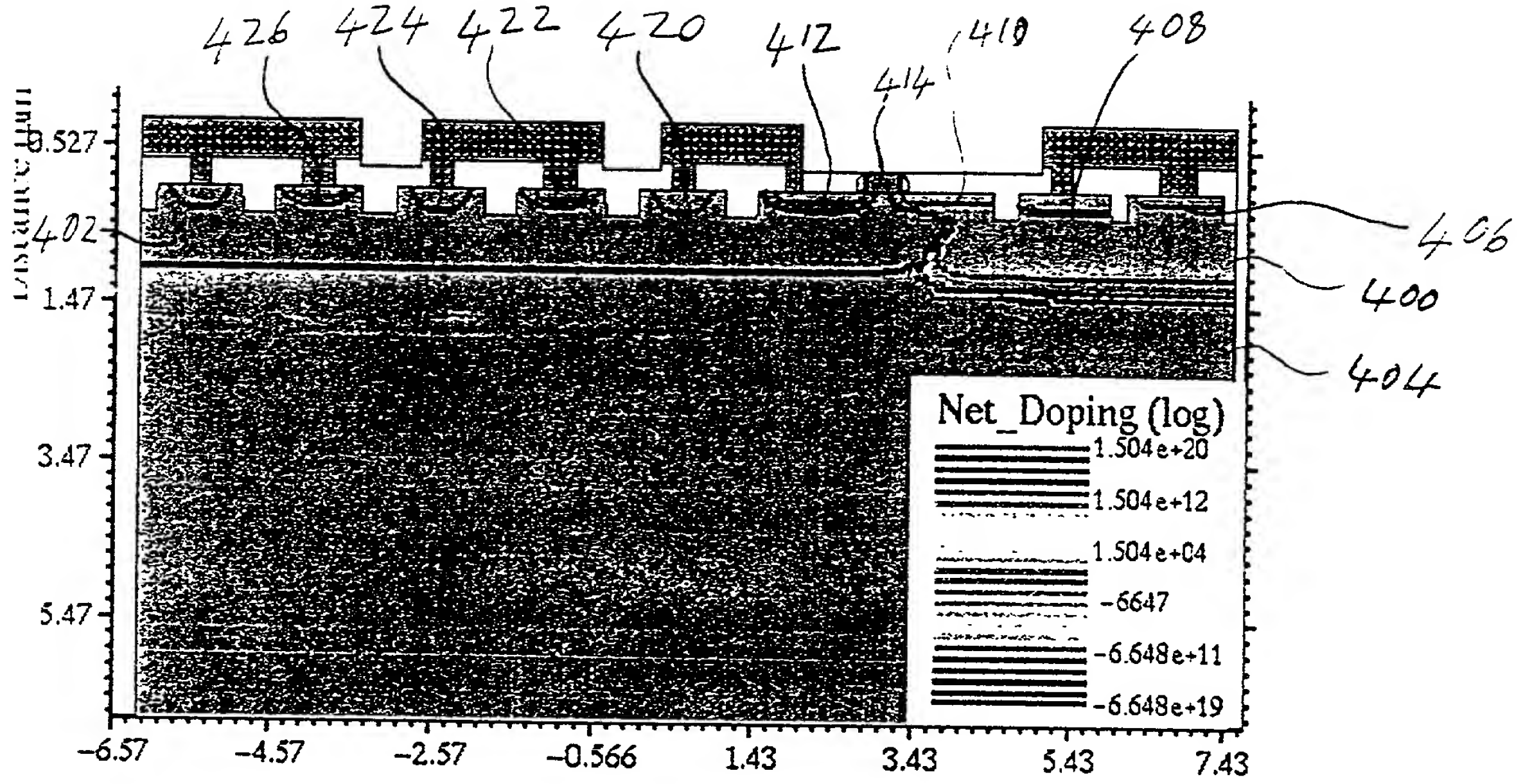


Fig 4

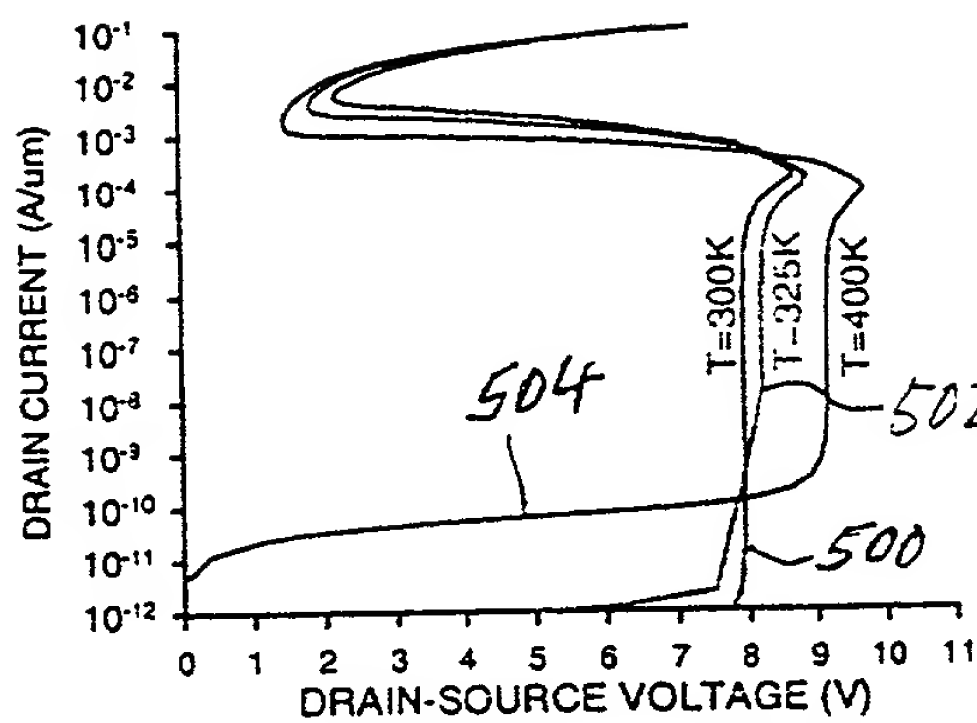


Fig 5

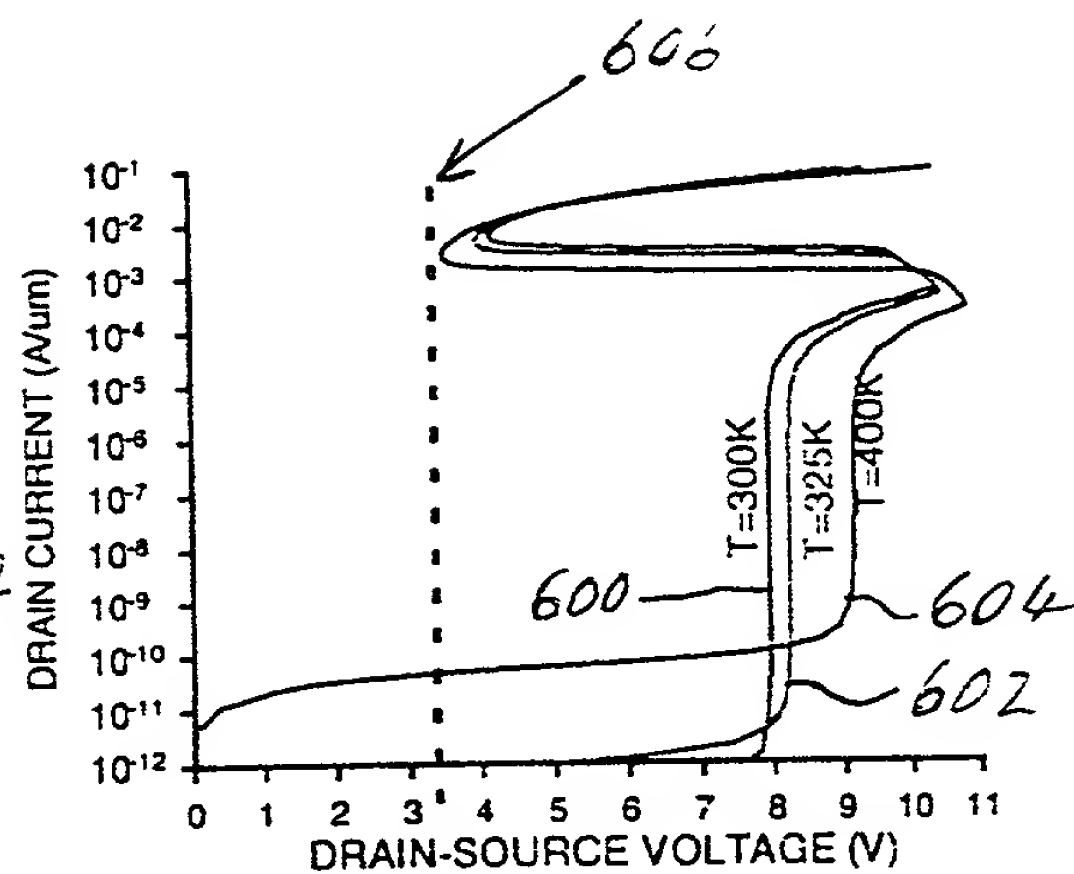


Fig 6

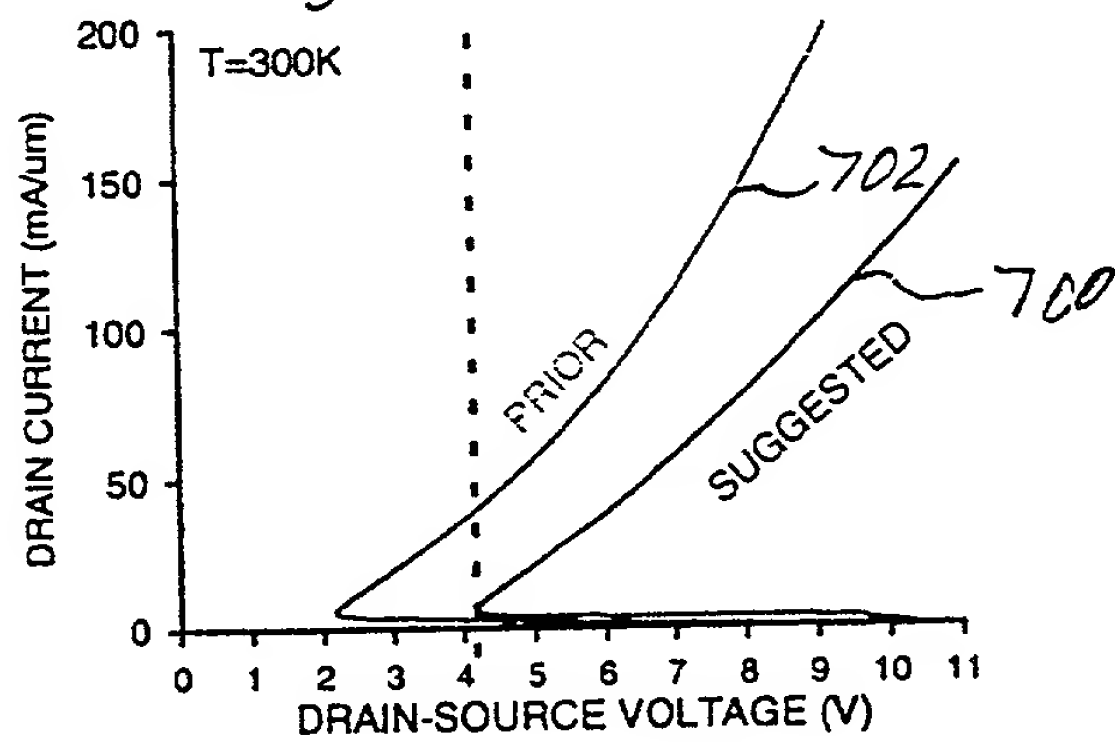
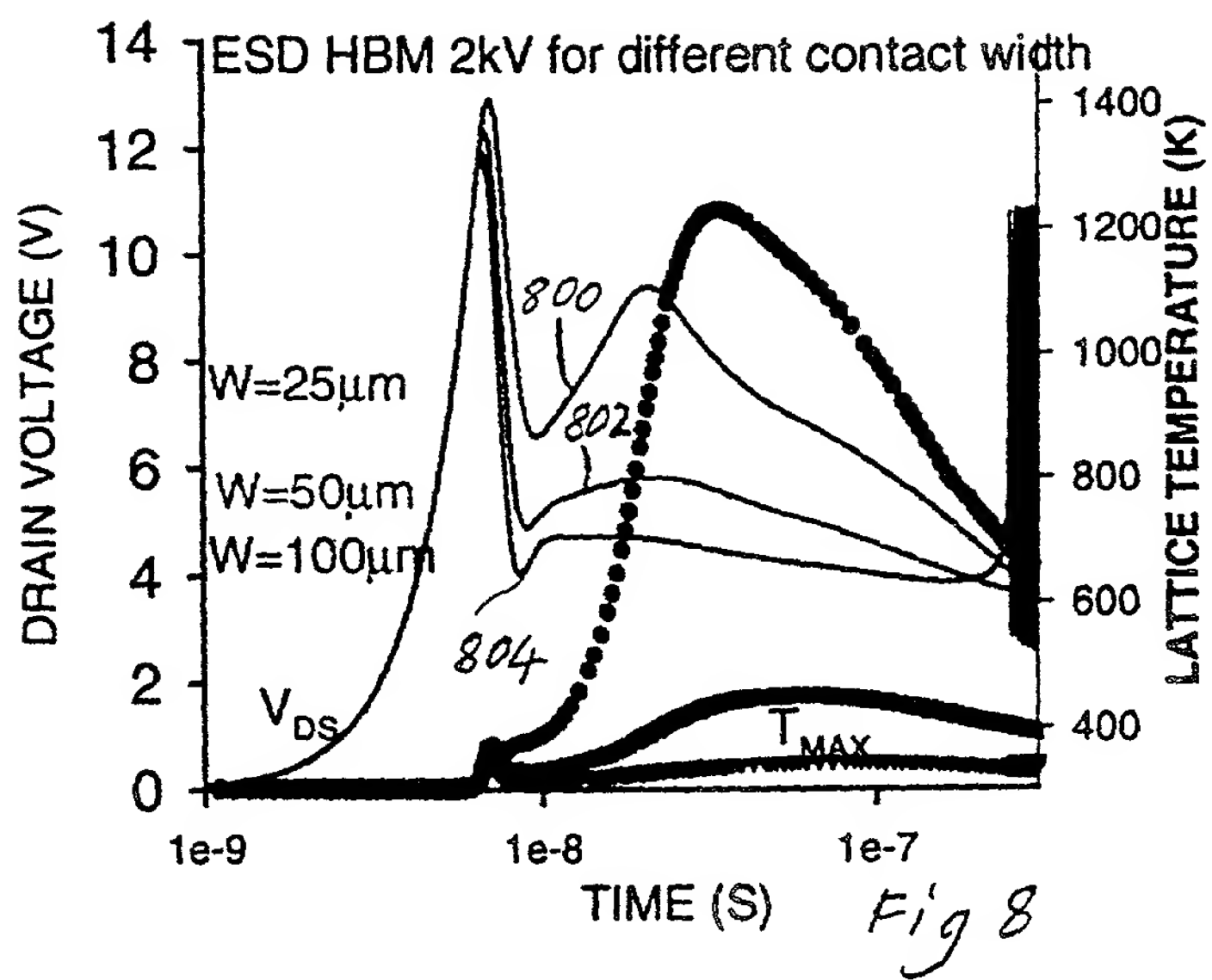


Fig 7

FOR 80" 324750



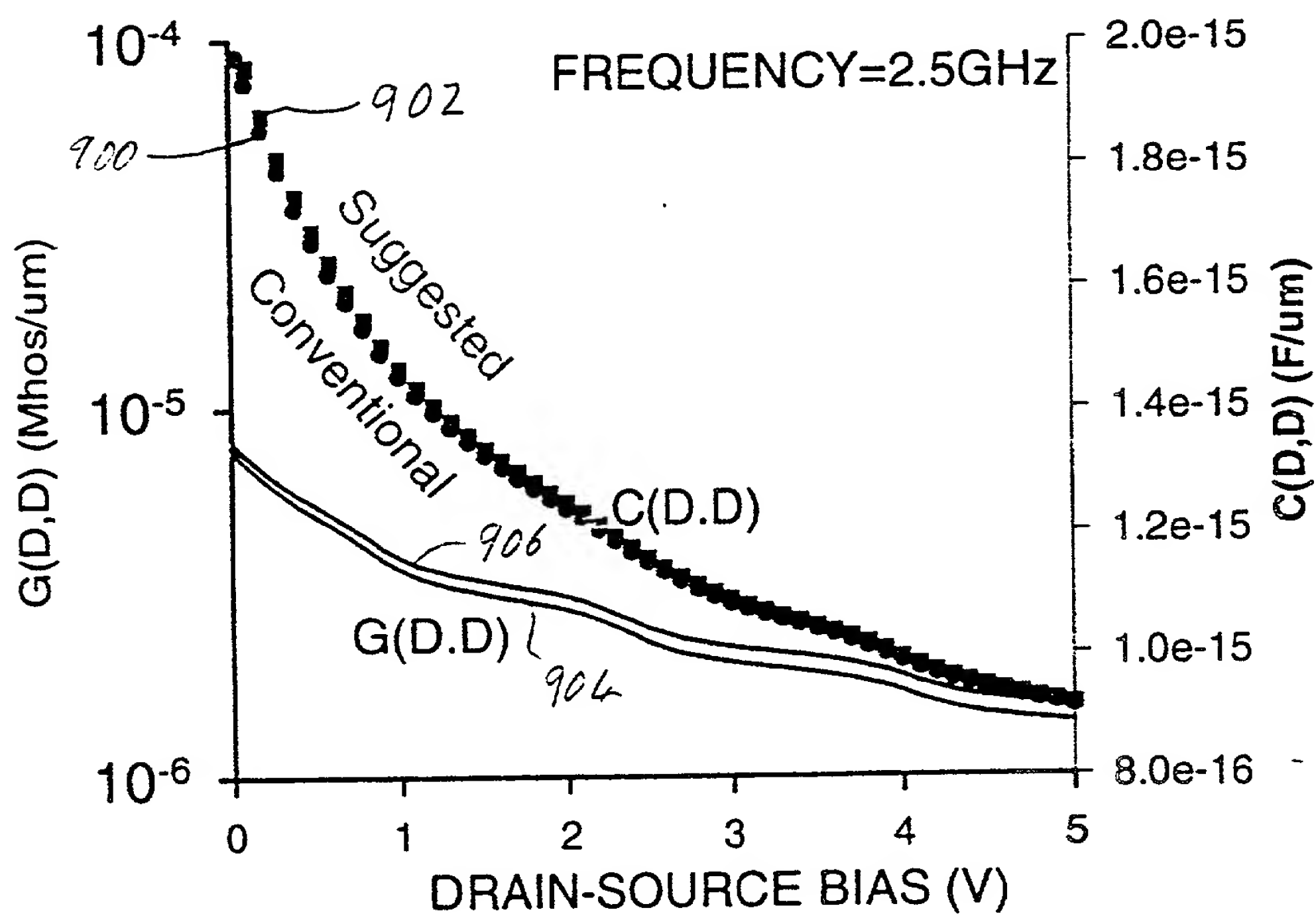


Fig 9

Fig 10

